

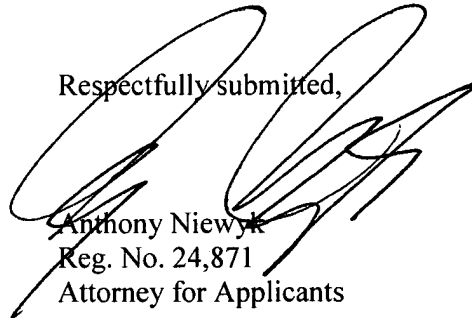
REMARKS

Entry of the present Supplemental Preliminary Amendment prior to the examination of the application is respectfully requested.

The specification, claims, and abstract have been amended to place them in U.S. format. No new matter has been added.

In the event Applicants have overlooked the need for an extension of time, an additional extension of time, payment of fee, or additional payment of fee, Applicants hereby petition therefore and authorize that any charges be made to Deposit Account No. 02-0385, Baker & Daniels LLP.

Respectfully submitted,



Anthony Niewyk
Reg. No. 24,871
Attorney for Applicants

AN/mh

BAKER & DANIELS LLP
111 East Wayne Street, Suite 800
Fort Wayne, Indiana 46802
Telephone: 260- 424-8000
Facsimile: 260-460-1700

Encs.: Substitute Specification
Redlined Specification
Abstract
Postcard



Method for Texturing Surfaces
of Silicon Wafers

METHOD FOR TEXTURING SURFACES
OF SILICON WAFERS

BACKGROUND OF THE INVENTION

[0001] The invention concerns a method for texturing surfaces of silicon wafers comprising the steps of dipping the silicon wafers in an etching solution of water, concentrated hydrofluoric acid and concentrated nitric acid and setting a temperature for the etching solution.

[0002] A method of this kind is known from the article "Isotropic texturing of polycrystalline silicon wafers with acidic texturing solutions," by R. Einhaus, E. Vazsonyi, J. Szlufcik et al., published in the Proceedings of the 26th PVSC, September 30 to October 3, 1997, Anaheim, California, U.S.A. In this prior method, polycrystalline silicon wafers are surface-textured in a temperature-controlled acidic etching solution comprising water, hydrofluoric acid in a concentration of 50%, and nitric acid in a concentration of 70% to reduce reflections that compromise efficiency.

[0003] Known from the documents US A 3,309,760, US 2002/0187583 A1, US A 5,681,398 and US A 5,174,855 are methods for treating surfaces of silicon wafers in order to prepare silicon for a metallization step, develop a structure, and, in the case of the last-cited documents, effect cleaning.

[0004] The object of the invention is to specify a method of the kind cited at the beginning hereof that is comparatively simple to perform, particularly on an industrial scale, and yields silicon wafers of improved efficiency.

SUMMARY OF THE INVENTION

[0005] This object is achieved according to the invention, in a method of the aforesaid kind, in that the etching solution comprises, in percent, 20% to 55% water, 10% to 40% concentrated hydrofluoric acid and 20% to 60% concentrated nitric acid and in that the temperature of the etching solution is between 0 and 15 degrees Celsius.

[0006] By virtue of the inventive composition of the etching solution comprising, in percent, 20% to 55% water, 10% to 40% concentrated hydrofluoric acid and 20% to 60% concentrated nitric acid, and additionally the fact that the etching process is carried out at a relatively low etching bath temperature well below room temperature, an appreciable improvement in efficiency is achieved. Moreover, the mixing of the etching solution, in the first place, proves to be relatively noncritical and can be performed in a relatively short period of time. In the second place, the etching process is relatively easy to monitor.

[0007] In an improvement of the inventive method, the etching solution comprises, in percent, 30% to 40% water, 15% to 30% concentrated hydrofluoric acid and 30% to 50% concentrated nitric acid.

[0008] In a further refinement of the inventive method, the temperature of the etching solution is between 7 and 9 degrees Celsius.

[0009] Advantageously, in the inventive method the silicon wafers remain in the etching solution for between 3 and 5 minutes.

[0010] In a particularly preferred exemplary embodiment of the inventive method, the etching solution comprises, in percent, 31% water, 23% concentrated hydrofluoric acid and 46% concentrated nitric acid, the temperature of the etching solution is 8 degrees Celsius, and the silicon wafers remain in the etching solution for between 1.5 and 2 minutes.

[0011] In one embodiment of the inventive method, the silicon wafers are oriented substantially vertically and the etching solution has a flow component. Both surfaces of the silicon wafers are thereby textured in substantially the same manner.

[0012] In a further advantageous embodiment of the inventive method, the silicon wafers are oriented substantially horizontally and the etching solution is quiescent. In this way, the upward-facing surface is textured particularly well by gas bubbles that effervesce away relatively quickly.

[0013] In a further improvement of the last-cited embodiment that is particularly advantageous for the continuous production of silicon wafers, the silicon wafers are moved through the etching solution. This results in particularly good texturing on both faces of the silicon wafers.

[0014] In the inventive method, the silicon wafers are advantageously polycrystalline.

~~Further advantageous embodiments of the invention are the subject of the following description of preferred exemplary embodiments of the invention, provided with reference to the figures of the drawing. Therein:~~

BRIEF DESCRIPTION OF THE DRAWINGS

[0015] The above mentioned and other features and objects of this invention, and the manner of attaining them, will become more apparent and the invention itself will be better understood by reference to the following description of embodiments of the invention taken in conjunction with the accompanying drawings, wherein:

[0016] Fig. 1 is a schematic diagram of the arrangement of silicon wafers in an inventive etching solution according to a first exemplary embodiment;

[0017] Fig. 2 is a schematic diagram of the arrangement of silicon wafers in an inventive etching solution according to a second exemplary embodiment;

[0018] Fig. 3 is a schematic diagram of the arrangement of silicon wafers in an inventive etching solution according to a third exemplary embodiment;

[0019] Fig. 4 is a graph of the qualitative dependence of average reflectance on etching time for an etching process employing an etching solution at room temperature and an etching process employing an inventive etching solution in the inventive temperature range;¹

[0020] Fig. 5 is a graph of the dependence of reflectance on wavelength for a silicon wafer textured in a conventional acidic etching solution and an inventively textured silicon wafer;¹ and

[0021] Fig. 6 is an electron microscope representation of one surface of an inventively textured silicon wafer.

[0022] Corresponding reference characters indicate corresponding parts throughout the several views. Although the exemplifications set out herein illustrate embodiments of the invention, in several forms, the embodiments disclosed below are not intended to be exhaustive or to be construed as limiting the scope of the invention to the precise forms disclosed.

DESCRIPTION OF THE PRESENT INVENTION

[0023] Figure 1 is a schematic diagram of a number of polycrystalline silicon wafers 1 implemented as “wafers”¹ and connected in a known manner to a holder 2. In the diagram of Fig. 1, holder 2, with the silicon wafers 1 oriented horizontally, is dipped in a tank 3 filled with a quiescent inventive etching solution 4. This horizontal arrangement causes gas bubbles that evolve on the upward-facing surface of the silicon wafers 1 during the etching process to effervesce upward, resulting in texturing on the upper surface of the silicon wafers 1 that is very effective in reducing reflection.

[0024] Figure 2 shows in schematic representation the arrangement of a silicon wafer connected to a holder 2 corresponding to the exemplary embodiment of Fig. 1, in an inventive etching solution 4 conforming to a second exemplary embodiment. In the second exemplary embodiment, the silicon wafers 1 are arranged vertically and the etching solution 4 has a flow

¹ ~~TRANSLATOR'S NOTE: The word “wafer” is given in English here, whereas all other occurrences of “wafer” in this document are translated from the German “Scheibe” (meaning disk, plate, pane, and, in semiconductor technology, wafer.)~~

component, indicated by dotted flow lines, along the silicon wafers 1; this is produced, for example, by moving the tank 3 back and forth or by the use of a stirring tool (not shown in Fig. 2) resistant to strong acids. In this way, gas bubbles forming on both surfaces of the silicon wafers 1 during the etching process are made to detach and effervesce upward at an early stage, causing both faces of the silicon wafers 1 to be textured uniformly.

[0025] Figure 3 shows in schematic representation the arrangement of silicon wafers 1 in an inventive etching solution 4 according to a third exemplary embodiment. In the exemplary embodiment of Fig. 3, a roller conveyor 5 is present that comprises a number of spacedly mutually confronting, at least partially driven upper rollers 6 and lower rollers 7 by means of which the silicon wafers 1 to be textured are conducted in a substantially horizontal path through the inventive etching solution 4 held in the tank 3. This exemplary embodiment is distinguished by being particularly suitable for the continuous production of silicon wafers 1 while providing very good texturing of both faces.

[0026] The composition of an exemplary inventive etching solution 4, comprising water, concentrated hydrofluoric acid – i.e., having a concentration of at least approximately 50% – and concentrated nitric acid – i.e., having a concentration of at least approximately 65%, – is as follows, it being understood that if other hydrofluoric acid and nitric acid concentrations are used, the proportions are to be adjusted accordingly:

- 2 parts water (H_2O),
- 1.5 parts hydrofluoric acid (HF, concentration 50%) and
- 3 parts nitric acid (HNO_3 , concentration 65%).

[0027] In the exemplary embodiments depicted in Figs. 1 to 3, the temperature of the etching solution 4 is maintained according to the invention between about 0 and about 15 degrees Celsius, preferably between about 7 and about 9 degrees Celsius.

[0028] Assuming undepleted etching solution 4, this results in a process time for texturing of about 3 minutes initially, increasing to about 5 minutes after the texturing of 550 silicon wafers 1 measuring $12.5 \times 12.5 \text{ cm}^2$ in roughly 12 liters of etching solution 4.

[0029] In a particularly preferred exemplary embodiment,

4 parts water (H_2O),
3 parts hydrofluoric acid (HF, concentration 50%) and
6 parts nitric acid (HNO_3 , concentration 65%)

are used, the temperature of the etching solution 4 being maintained at about 8 degrees Celsius. The process time in this particularly preferred exemplary embodiment is between 1.5 and 2 minutes.

[0030] The etching process is monitored by weighing textured silicon wafers 1 before and after the etching process, the correct duration of the etching process being indicated by a set nominal weight loss that depends on the size of the silicon wafer 1 concerned and the desired material removal. If the actual weight loss is below the nominal weight loss, the duration of the etching process is increased until the actual weight loss equals the nominal weight loss. Correspondingly, if the actual weight loss is above the nominal weight loss, the duration of the etching process is decreased until the actual weight loss equals the nominal weight loss.

[0031] Figure 4 is a graph of the qualitative dependence of average reflectance on etching time for a comparison method using an etching solution at room temperature and for an etching process using an inventive etching solution within the inventive temperature range.

[0032] In the graph of Fig. 4, the etching time is plotted on the abscissa 8. Plotted on the ordinate 9 is the average reflectance, averaged over a wavelength range of 400 nm to 1100 nm. The values approximated by a first approximation curve 10 derive from silicon wafers 1 textured by the inventive method. The measurement values approximated by a second approximation curve 11 derive from silicon wafers textured by a comparison method.

[0033] A comparison of the first approximation curve 10, pertaining to the method according to the invention, with the second approximation curve, for the comparison method, reveals that the average reflectance in the case of inventively textured silicon wafers 1 is appreciably lower than that obtained with silicon wafers textured by the comparison method.

[0034] Figure 5 shows, in a graph, the dependence of reflectance on wavelength for a silicon wafer textured in a conventional acidic etching solution and an inventively textured silicon wafer 1.

[0035] In the graph of Fig. 5, the wavelength in nanometers is plotted on the abscissa 12 and the reflectance in percent on the ordinate 13. The empty circles represent measurement values from silicon wafers 1 textured by the inventive method using the above-specified exemplary etching solution 4, while the measurement values represented by solid squares refer to a conventional method developed by IMEC (hereinafter referred to as the “IMEC comparison method”) as described in the article “Towards highly efficient industrial cells and modules from polycrystalline wafers,” by F. Duerinckx, L. Frisson, P.P. Michiels et al., published at the 17th European Photovoltaic Solar Energy conference, October 22-26, 2001, Munich, Germany.

[0036] In the IMEC comparison method, the etching solution comprises HF, HNO₃ and several additives. The temperature of the etching solution is 21 degrees Celsius. The silicon wafers remain in the etching solution for 3 minutes.

[0037] It will be appreciated from the graph of Fig. 5 that the reflection values obtained with inventively textured silicon wafers 1, which yield an average reflectance of 23.8%, particularly beginning at wavelengths of about 600 nm, are up to about 2.5% lower than those obtained with silicon wafers textured according to the IMEC comparison method, which exhibited an average reflectance of 24.9%.

[0038] The following table juxtaposes important properties for the inventively textured silicon wafers 1 (UKN isotexture) and silicon wafers textured by the IMEC comparison method (IMEC isotexture), compared in each case to texturing with a basic NaOH solution.

	UKN Isotexture				IMEC Isotexture			
	FF %	J _{sc} mA/cm ²	V _{oc} mV	Eta %	FF %	J _{sc} mA/cm ²	V _{oc} mV	Eta %
NaOH	76.3	31.2	615.9	14.6	77.9	30.5	620	14.8
Isotexture	76.6	33.2	614.1	15.6	77.9	31.6	614	15.2
Relative improvement [%]	0.4	5.4	-0.3	6.8	0	3.6	-1	2.7

[0039] The data for the IMEC isotexture are derived from Duerinckx et al.

[0040] For each of the above sets of silicon wafers:

FF = fill factor,
J_{sc} = short-circuit current density,
V_{oc} = open terminal voltage and
Eta = efficiency.

[0041] Figure 6 is an enlarged photographic depiction of one surface of an inventively textured silicon wafer 1, with a scale notation. It can be seen from Fig. 6 that texturing according to the inventive method leads in part to comparatively elongated, ribbon-like depressions in a distinctly reflection-reducing surface structure.

[0042] It is understood that the inventive method can also be used with monocrystalline silicon wafers or silicon wafers pulled directly from the melt, so-called sheet silicon.

[0043] While this invention has been described as having a preferred design, the present invention can be further modified within the spirit and scope of this disclosure. This application is therefore intended to cover any variations, uses, or adaptations of the invention using its general principles. Further, this application is intended to cover such departures from the present disclosure as come within known or customary practice in the art to which this invention pertains and which fall within the limits of the appended claims.